

Student Technician - Slicer

Position Type

Part-time/Non work-study

Description

Part time Student Technician positions to work with full-time staff to develop an in-house ASIC development capability using Open Source tools (including Yosys, Graywolf, Qflow, Magic, OpenRAM) and design an ASIC for fabrication. Additional assignments include RTL to layout design, verification analysis, design trade-off studies, software bug resolution, and research documentation. Will utilize the following technologies: Verilog, C/C++, Python, Tcl, LEF/DEF, shell scripts, GNU Make, Git.

Qualifications

- U.S. Citizenship required.
- Applicant selected will be subject to a government security investigation and must meet eligibility requirements for access to classified information at the level appropriate to the project requirements of the position.
- Prefer undergraduate student pursuing a degree in Electrical Engineering or Computer Science or other related.
- GPA greater than or equal to 3.0.
- Completion of EE460R Introduction to VLSI Design or EE460M Digital System Design is preferred but not required.

Approximate Hours per Week

10-15

Hourly Wage

\$15.13/hr or more based on student classification and relevant experience.

Contact Information

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